

Modelsim Manual

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This paper presents the development of an IP core for an H.264 decoder. This state-of-the-art video compression standard contributes to reduce the huge demand for bandwidth and storage of multimedia ...

IP Core for an H.264 Decoder SoC

They also resulted in manual errors; CPU and license time gets enormously wasted ... Code coverage can be done with options provided in the simulators like Modelsim from Model Technology or Ncsim from ...

Reusability and Modularity in SoC Verification

We also needed to replace an inefficient and error-prone verification process that required importing and exporting text files and many other manual steps." Lockheed Martin engineers used MATLAB ...